





FORM PTO-1449  
Equivalent

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO. 500.32032CC6  
SERIAL NO. 09/132,085  
APPLICANT: K. Matsubara, et al.  
FILING DATE: August 10, 1998  
GROUP: 2818

U.S. PATENT DOCUMENTS

EXAMINER INITIALS	DOCUMENT NO.	DATE	NAME	CLASS SUBCLASS	FILING DATE IF APPROPRIATE
gu	5,199,001	3/93	TZENG	365/218	
gu	5,255,237	10/93	KODAMA	365/218	
gu	5,418,742	5/95	HARARI, ET AL.	365/218	
gu	5,390,146	2/95	ATWOOD, ET AL.	—	

FOREIGN PATENT DOCUMENTS

DOCUMENT NO.	DATE	COUNTRY	CLASS SUBCLASS	TRANSLATION
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OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)

Examiner

Vu Le

Date Considered

5/3/99

\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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U.S. PATENT DOCUMENTS

EXAMINER INITIALS	DOCUMENT NO.	DATE	NAME	CLASS SUBCLASS	FILING DATE IF APPROPRIATE
<i>ju</i>	4,975,878	12/90	BODDU, ET AL.		ALL OF
<i>ju</i>	5,199,001	3/93	TZENG		TECH.
<i>ju</i>	5,255,237	10/93	KODAMA		TECH.
<i>ju</i>	5,418,742	5/95	HARARI, ET AL.		TECH.
<i>ju</i>	5,390,146	2/95	ATWOOD, ET AL.		TECH.

FOREIGN PATENT DOCUMENTS

DOCUMENT NO.	DATE	COUNTRY	CLASS SUBCLASS	TRANSLATION

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Examiner

*Vu Le*

Date Considered

*7/17/01*

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7/30/99

Sheet 1 of 1

Form PTO-1449  U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE  <b>INFORMATION DISCLOSURE STATEMENT          BY APPLICANT</b>  (Use several sheets if necessary)	ATTY. DKT. NO. 500.32032CC6	SERIAL NO. 09/132,085
	APPLICANT K. MATSUBARA, et al.	
	FILING DATE 8/10/98	GROUP 2818

## U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date
BM	AA 4,794,558	12/27/88	Thompson	364	900	9/25/86
BM	AB 5,097,445	3/17/92	Yamauchi	365	195	12/14/89
	AC					
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	AL					

## FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
BM	AM 0 489 204 A1	6/10/92	European				
BM	AN 0 183 232 A2	6/4/86	European				
BM	AO 0 447 856 A1	9/25/90	European				
	AP						
	AQ						
	AR						
	AS						
	AT						

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	AU	
	AV	
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Examiner	Vu Le	Date Considered 7/17/01

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	AA					
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## FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
	AM						
	AN						
	AO						
	AP						
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194	AU	Block Structure in Flash Memory, Electronic Engineering 63 (1991) July, No. 775, Woolwich, London, GB
	AV	
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Examiner Vale		Date Considered 7/17/01

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	<b>APPLICANT</b> K. Matsubara, et al.	
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## U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date
Ju	AA 5,602,738	2/11/97	Sasaki	—	—	
	AB					
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	AL					

## FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
Ju	AM 63-206852	8/26/88	Japan	—	—		
Ju	AN 2-89555	7/16/90	Japan	—	—		
Ju	AO 1-298600	12/1/89	Japan	—	—		
Ju	AP 1-273296	11/1/89	Japan	—	—		
Ju	AQ 62-117633	6/25/87	Japan	—	—		
Ju	AR 2-310786	12/26/90	Japan	—	—	X	
Ju	AS 3-229955	10/11/91	Japan (USP 5,602,739	—	—		
	AT		is counterpart)				

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Ju	AU	Ko-Min Chang, "A Modular Flash EEPROM Technology For 0.8 $\mu$ m High Speed Logic Circuits", IEEE 1991 Custom Integrated Circuits Conference, pp. 18.7.1-18.7.4.				
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